phyCORE-i.MX 6 (PCM-058) Changes of the Pinout from PCB revision 1429.2 on

The information in this document is relevant for PHYTEC SOM phyCORE-i.MX 6 (PCM-058).

Signal X_PMIC_STBY_REQ at pin C27 of the phyCORE-Connector X1 is changed to signal X_3V3_GOOD. Which means that X_PMIC_STBY_REQ is no longer available on the phyCORE Connector.

The new signal X_3V3_GOOD indicates that the 3.3 V logic voltage on the module is available and stable. It should be used to turn on devices, or enable level shifters on the carrier board which are connected to the phyCORE-i.MX 6. This prevents voltages at the phyCORE-i.MX 6's IO pins which are sourced from supply voltages of peripheral devices attached to the SOM. These voltages can cause a current flow into the controller especially if peripheral devices attached to the interfaces of the i.MX 6 are supposed to be powered while the phyCORE-i.MX 6 is in suspend mode, or turned off. In addtion, use of signal X_3V3_GOOD supports to fulfill the power on sequence of the i.MX 6.

1429.1:

Pin #	Signal	ST	Voltage Domain	Description
X1C27	X_PMIC_STBY_REQ	0	VDD_MX6_SNVS	i.MX 6 PMIC standby request

1429.2 and successors:

Pin #	Signal	ST	Voltage Domain	Description
X1C27	X_3V3_GOOD	0	VDD_3V3	Indicates presence of 3.3 V logic voltage

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